

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Akram et al.

**Serial No.:** Not yet assigned

**Filed:** December 28, 2001

**For:** MULTI-CHIP MODULE SYSTEM  
AND METHOD FOR FABRICATION

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2754.4US  
(95-0742.4)

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**INFORMATION DISCLOSURE STATEMENT**

Box PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

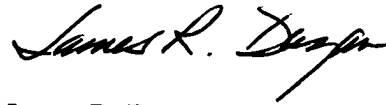
This application is a divisional of application Serial No. 09/102,224, filed June 22, 1998, pending, which is a continuation of application Serial No. 08/614,301, filed March 12, 1996, now United States Patent 5,807,762, issued September 15, 1998.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

**Attorney Docket No. 2754.4US (95-0742.4)**

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: December 28, 2001

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Enclosure: Form PTO-1449

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